

PATENT NUMBER

U.S. POSTAGE 986

09/842332

04/24/01

| ISSUE CLASSIFICATION | |
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| Class | Subclass |
| | |

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

G.A.

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|------------------------------|------------|--------------|-----------------|-------------------|--------------------|
| APPLICATION NO. 09/842332 | CONT/PRIOR | CLASS 375 | SUBCLASS 354 | ART UNIT 2631+ | EXAMINER TSE HA |
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APPLICANTS

Table

Wai Fong
Jyh-Ming Jong
Leo Yuan
Brian Smith

Source: synchronous receiver link initialization and input floating control by clock detection and DLL lock detection

PTO-2040
12/89**ISSUING CLASSIFICATION**[illegible]

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|---|--|-------------|------------|-----------------------------------|----------------------|
| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| | | | | | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |

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Form **PTO-436A**
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